

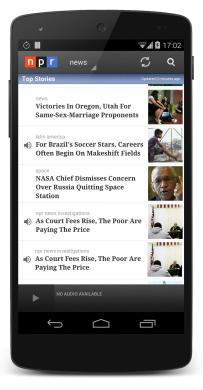
Scalable Race Detection for Android Applications

Pavol Bielik, Veselin Raychev, Martin Vechev

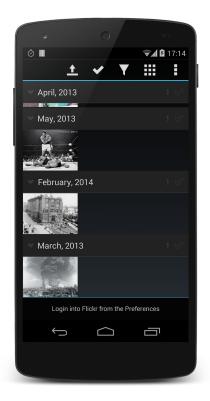
Software Reliability Lab Department of Computer Science ETH Zurich



Errors Caused by Concurrency









Display article twice

Display wrong directory

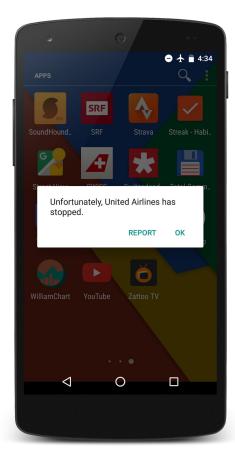
Display wrong order

Rate wrong card

Errors Caused by Concurrency









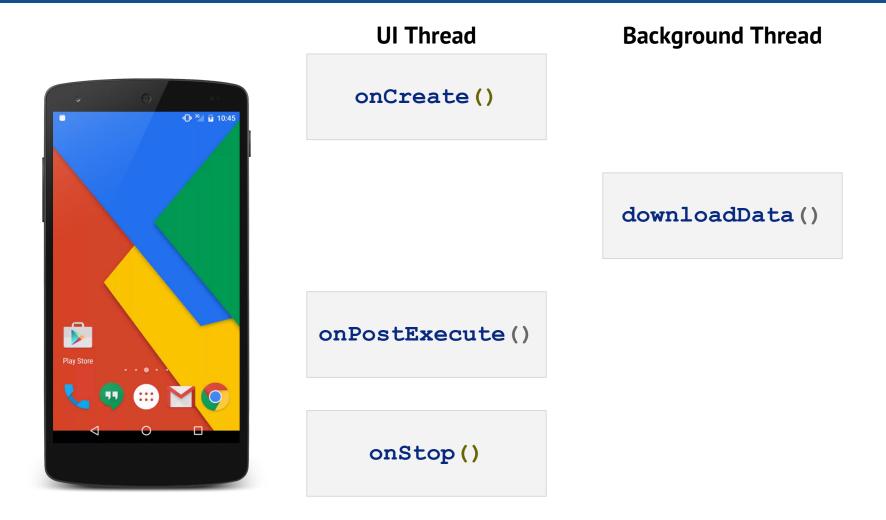
UI Thread

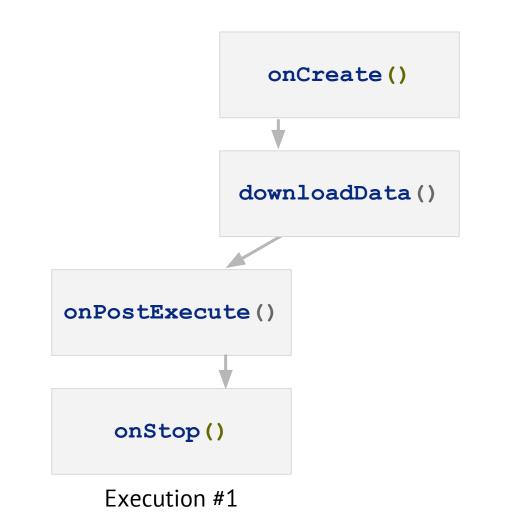


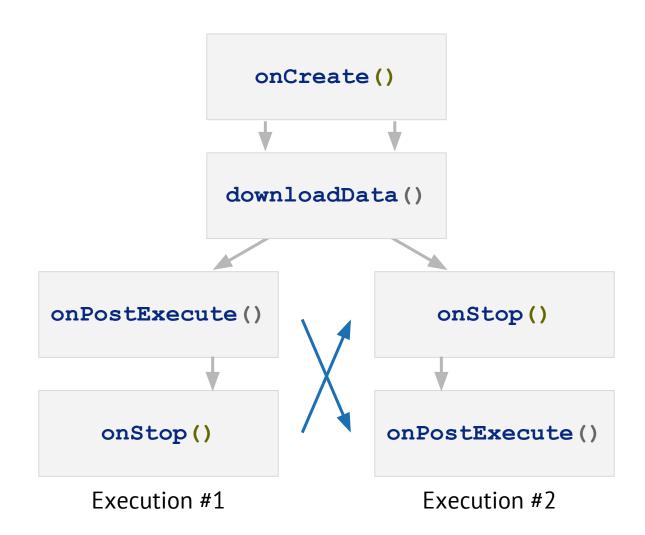
onCreate()











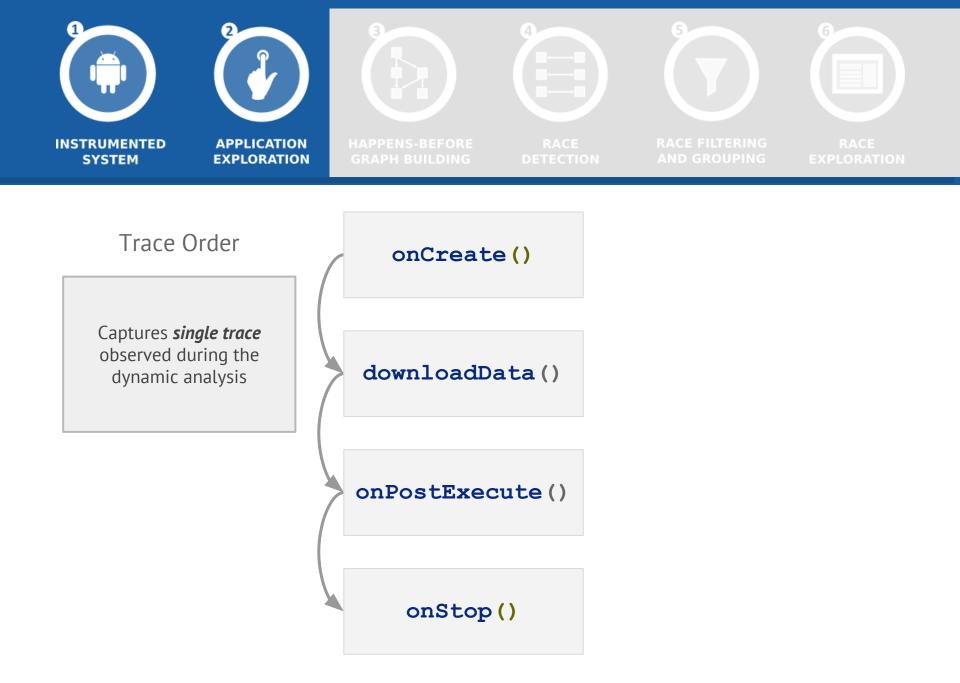
Online analysis tool

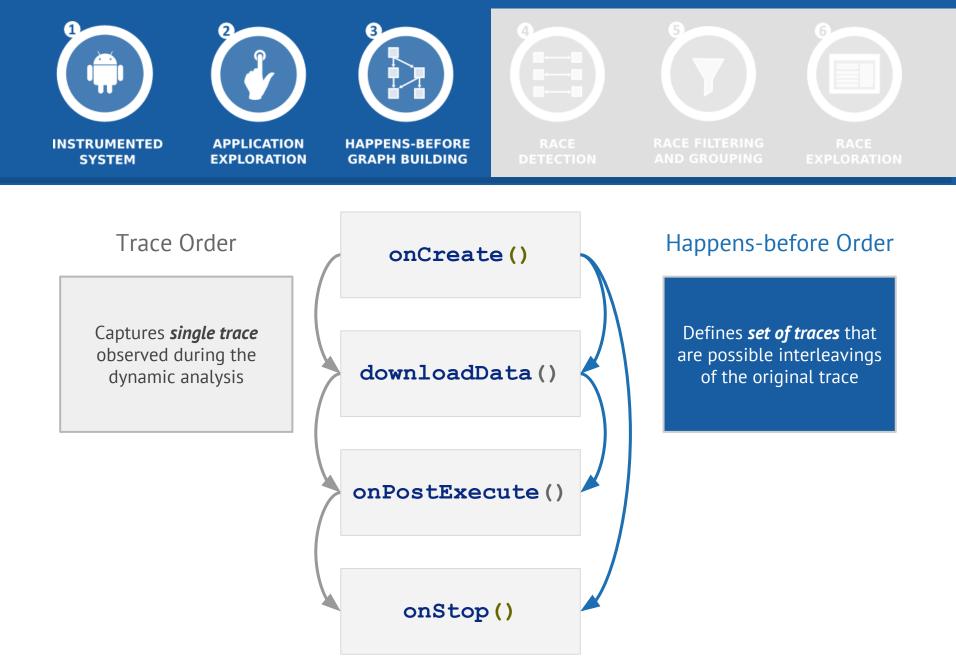


Select Android application APK file for analysis

Choose File No file chosen









SYSTEM



APPLICATION EXPLORATION



HAPPENS-BEFORE

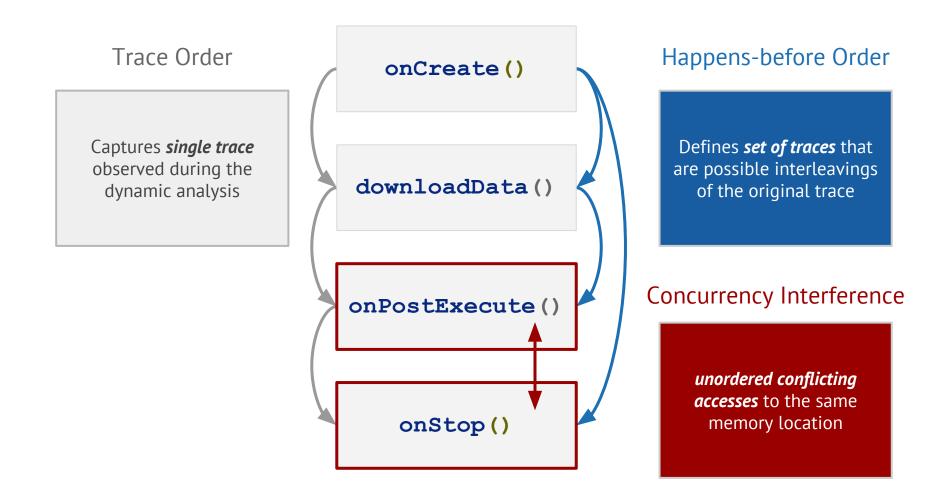
GRAPH BUILDING



RACE DETECTION









SYSTEM



APPLICATION EXPLORATION

HAPPENS-BEFORE

GRAPH BUILDING



RACE DETECTION

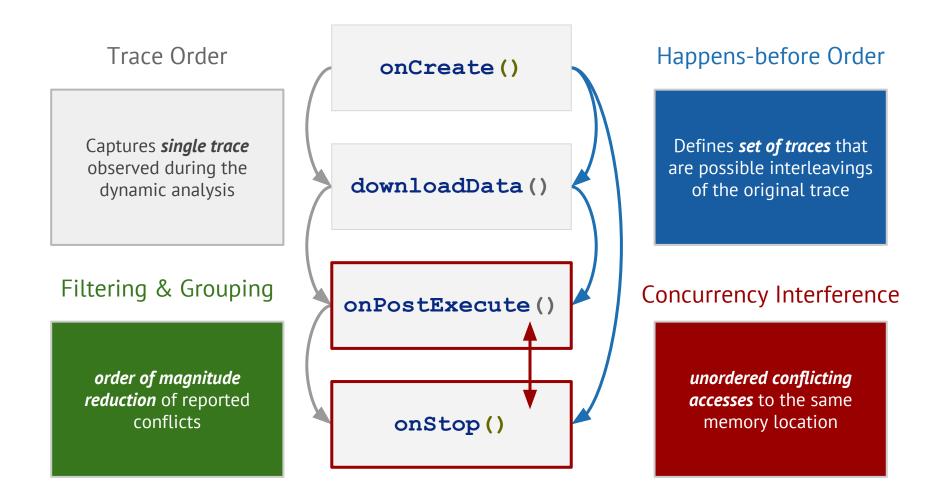


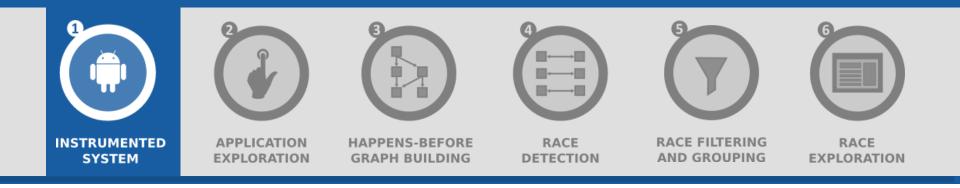
RACE FILTERING

AND GROUPING



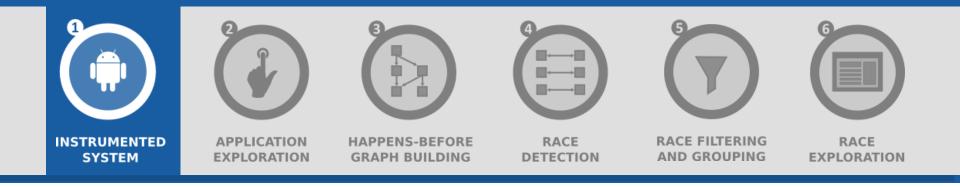
RACE EXPLORATION



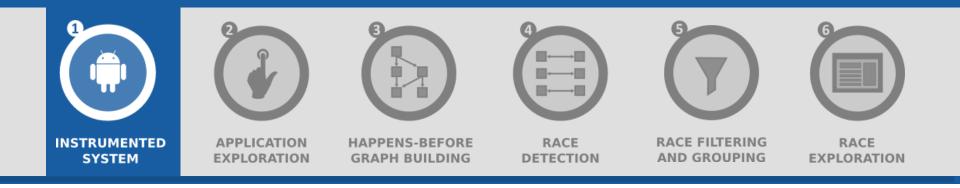


void onCreate() {
 new Thread(...).start();

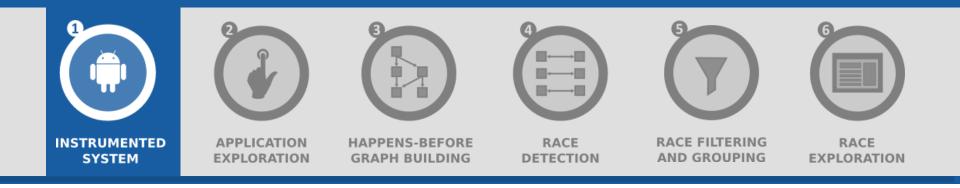
void downloadData() {
 postDelayed(..., 100);



begin(...)
void onCreate() {
 new Thread(...).start();
}
end(...)
void downloadData() {
 postDelayed(..., 100);
}



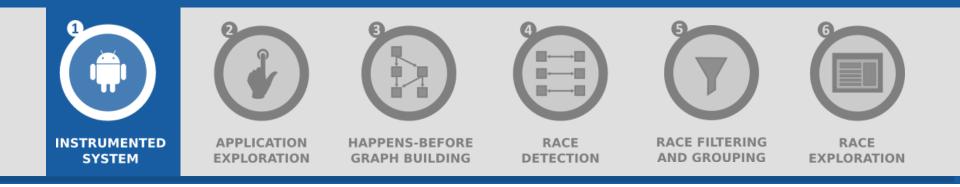
begin(...)
 void onCreate() {
 new Thread(...).start();
 }
 end(...)
begin(...)
 void downloadData() {
 postDelayed(..., 100);
 }
end(...)



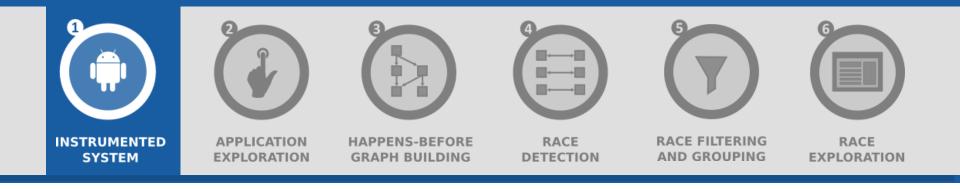
begin(...)
 void
 fork(...)
 end(...)
 begin(...)
 void
enqueue(...)
 po
}
end(...)

void onCreate() {
 new Thread(...).start();

void downloadData() {
 postDelayed(..., 100);



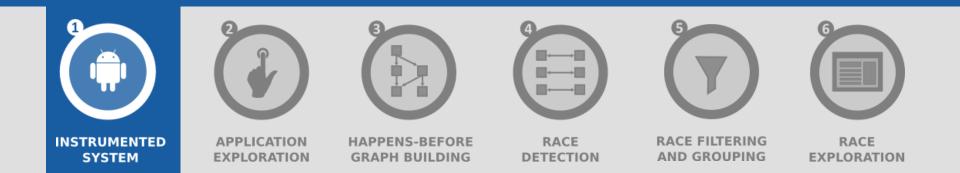
	begin(…)	<pre>void onCreate() {</pre>
mapping of all	fork()	<pre>new Thread().start();</pre>
Android APIs into 11	end() begin()	5
operations		<pre>void downloadData() { postDelayed(, 100);</pre>
	enqueue () end ()	}



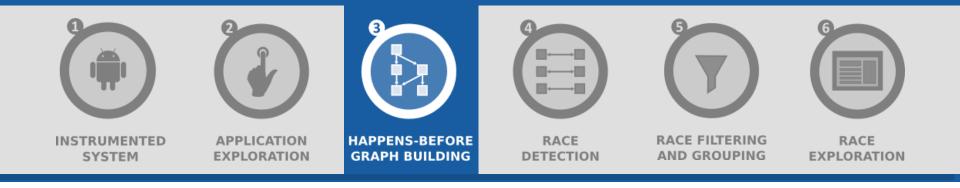
What are the memory locations on which events can interfere?

- → Object and Class fields
- → High level operations

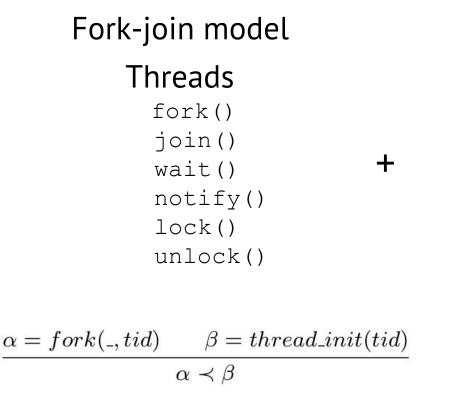
```
void onPostExecute() {
  mDatabase.insert();
}
READ 23867 mDbHelper
WRITE TABLE:Users ID:2
WRITE TABLE:Users ID:3
...
```



Instrumentation of **both application and framework** with overhead only ~300%



What is the event happens-before?



Rich Event-Based model

Message Queue

postDelayed(delay)
postAtTime(time)
postFront()
postIdle()
remove()

 $\begin{array}{ll} \alpha = end(mid) & \eta = enqueue(mid) \\ \beta = begin(mid') & \gamma = enqueue(mid') & \eta \prec \gamma \\ looper_{ord}(\eta, \gamma) & \eta.dispatcher = \gamma.dispatcher \\ \eta.type \in \{Delayed, AtTime, Front, Idle\} \\ \gamma.type \in \{Delayed, AtTime, Idle\} \\ \eta.barrier \lor \neg \gamma.barrier \\ \end{array}$



$$\begin{array}{c|c} \hline \alpha = vent \neq \bot & \alpha event = \beta event & \alpha < \pi \beta \\ \hline \alpha \prec \beta & \eta < \eta \\ \hline \alpha \prec \beta & \eta < \eta \\ \hline \alpha \prec \beta & \eta < \eta \\ \hline \alpha & \neg \beta & \eta \\ \hline \alpha & \neg \beta & \eta \\ \hline \alpha & \neg \beta & (CALLBACKREG#1) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKREG#2) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKREG#2) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNREG) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNREG) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNREG) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNREG) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNREG) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNREG) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNREG) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNREG) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNREG) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNN) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNN) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNN) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNN) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNN) & \eta \\ \hline \alpha & \neg \beta & (CALLBACKUNNEG) & \eta \\ \hline \alpha & \neg \beta$$

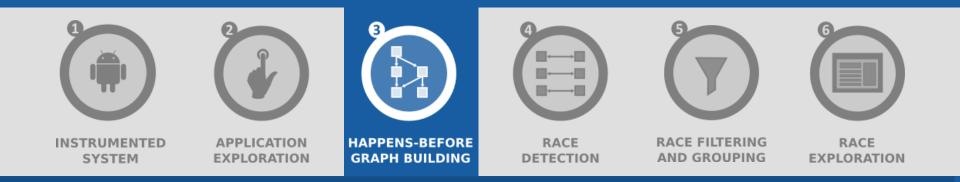
$$\begin{split} \eta &= begin(mid) & \alpha &= end(mid) \\ \beta &= begin(mid') & \gamma &= end(mid') \\ \eta &\prec \gamma & \alpha.dispatcher &= \beta.dispatcher \\ \hline \alpha.dispatcher_{type} &= LOOPET \\ \hline \alpha.dispatcher_{type} &= LOOPET \\ \hline \alpha.dispatcher_{type} &= LOOPET \\ \hline \alpha.ds & \beta \\ \hline \\ \hline \frac{\alpha &= enqueue(mid) & \beta &= begin(mid) \\ \alpha.ds & \alpha &< \beta \\ \hline \\ \frac{\alpha &= fork(\neg, tid) & \beta &= thread_init(tid) \\ \alpha.ds & \alpha &< \beta \\ \hline \\ \frac{\alpha &= thread_exit(tid) & \beta &= join(\neg, tid) \\ \alpha.ds & \alpha &< \beta \\ \hline \\ \frac{\beta &= thread_exit(tid) & \beta &\in \pi_{tid} \setminus \alpha \\ \alpha.ds & \alpha &< \beta \\ \hline \\ \frac{\beta &= thread_exit(tid) & \alpha &\in \pi_{tid} \setminus \beta \\ \alpha.ds & \alpha &< \beta \\ \hline \\ \frac{\alpha &= notify(id) & \beta &= wait(id) \\ \alpha.ds & \alpha &< \beta \\ \hline \\ \frac{\alpha &= begin(mid) \\ ext{ remove(mid) } & \gamma &= enqueue(mid) \\ \alpha.ds & \alpha &< \beta \\ \hline \\ \frac{end(mid) & \beta &= blocking_enqueue_end(mid) \\ \alpha.ds & \alpha &< \beta \\ \hline \end{array}$$
 (MSGREMOVE) \\ \hline \end{array}

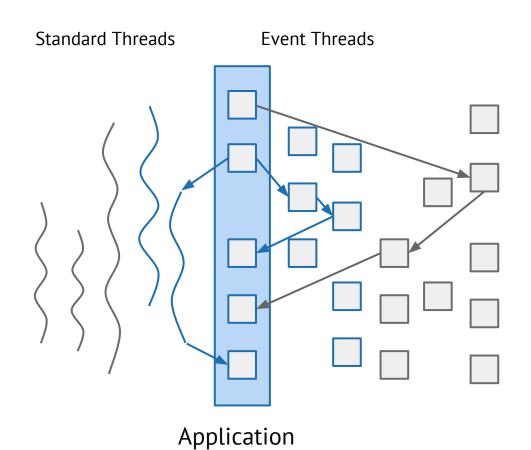
$$\begin{aligned} \alpha &= end(mid) <_{\pi} \beta = begin(mid') \\ \alpha.type, \beta.type \in \{Input, Display\} \\ \underline{\alpha.type, \beta.type} \quad \alpha.dispatcher = \beta.dispatcher \\ \alpha \prec \beta \end{aligned} \tag{NATIVE} \\ \hline \begin{array}{c} \alpha &= end(mid) \quad \eta = enqueue(mid) \\ \beta &= begin(mid') \quad \gamma = enqueue(mid') \quad \eta <_{\pi} \gamma \\ \eta.sync \lor \neg \gamma.sync \quad \eta.type = \gamma.type = IPC \\ \overline{\eta.pid = \gamma.pid} \quad \eta.dispatcher = \gamma.dispatcher \\ \alpha \prec \beta \end{aligned} \tag{IPCHANDLE} \\ \hline \begin{array}{c} \alpha, \beta \in \pi_{tid} \quad \alpha <_{\pi} \beta \quad \alpha.event = \beta.event = \bot \\ \alpha \prec \beta \end{aligned} \tag{IPCHANDLE} \\ \hline \alpha &= end(mid) \quad \eta = enqueue(mid) \\ \alpha &= end(mid) \quad \eta = enqueue(mid) \end{aligned}$$

$$\begin{array}{ll} \beta = begin(mid') & \gamma = enqueue(mid') & \eta \prec \gamma \\ \eta.type = \gamma.type = IPC & \neg\eta.sync & \neg\gamma.sync \\ \eta.tid = \gamma.tid & \eta.dispatcher = \gamma.dispatcher \\ \hline \alpha \prec \beta \end{array} (IPCASYNC)$$

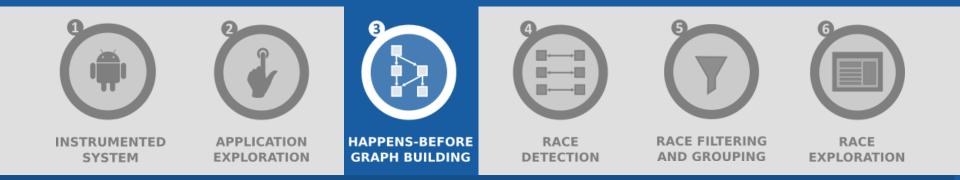


Thorough and precise happens-before model which captures Android concurrency





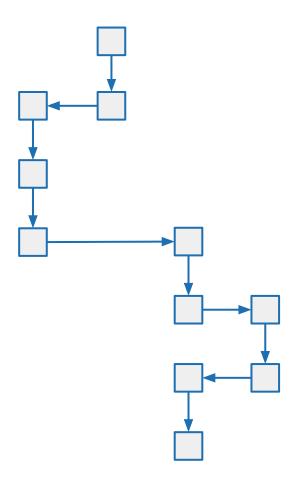
Framework & Other Applications

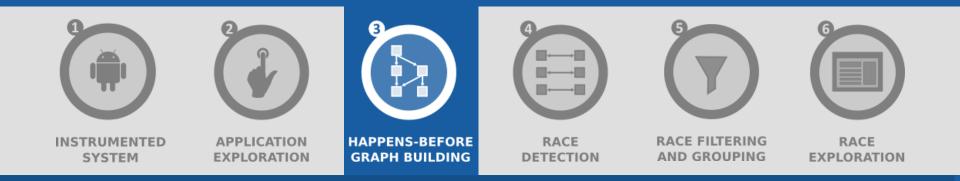


How to efficiently build the happens-before graph?

Key Scalability Ingredients:

- → Efficient Rule Matching
- → Sparse Graph
- → Fast connectivity queries
- → Evaluating rules only once
- → Trace optimization
- → Graph traversal pruning

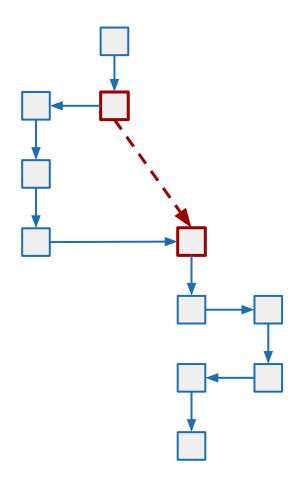




How to efficiently build the happens-before graph?

Key Scalability Ingredients:

- → Efficient Rule Matching
- → Sparse Graph
- → Fast connectivity queries
- → Evaluating rules only once
- → Trace optimization
- → Graph traversal pruning





SYSTEM



INSTRUMENTED **APPLICATION EXPLORATION**



HAPPENS-BEFORE **GRAPH BUILDING**





RACE DETECTION

RACE FILTERING AND GROUPING

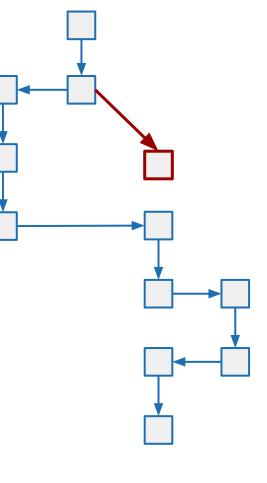


RACE **EXPLORATION**

How to efficiently build the happens-before graph?

Key Scalability Ingredients:

- Efficient Rule Matching →
- Sparse Graph \rightarrow
- \rightarrow Fast connectivity queries
- **Evaluating rules only once →**
- Trace optimization \rightarrow
- \rightarrow Graph traversal pruning



EventOp CallbackReg#1 CallbackReg#2 CallbackUnreq CallbackInv MsqBegin#1 MsqBegin#2 LooperAtomic MsgEnqueue ThreadFork ThreadJoin ThreadInit ThreadExit NotifyWait MsqRemove MsgBlocking Native IpcHandle ThreadOp IpcAsync





INSTRUMENTED SYSTEM

APPLICATION



HAPPENS-BEFORE GRAPH BUILDING



RACE DETECTION



RACE FILTERING

AND GROUPING

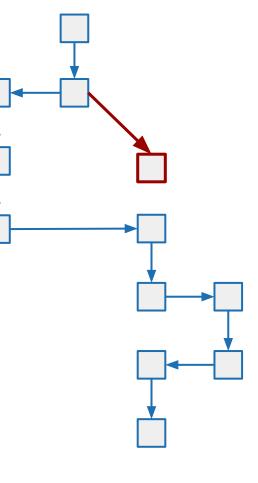


RACE EXPLORATION

How to efficiently build the happens-before graph?

Key Scalability Ingredients:

- → Efficient Rule Matching
- → Sparse Graph
- → Fast connectivity queries
- → Evaluating rules only once
- → Trace optimization
- → Graph traversal pruning

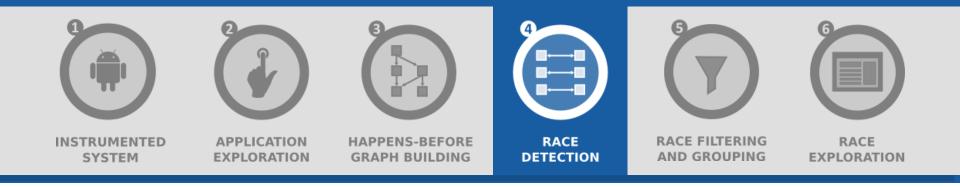


re-evaluate rules

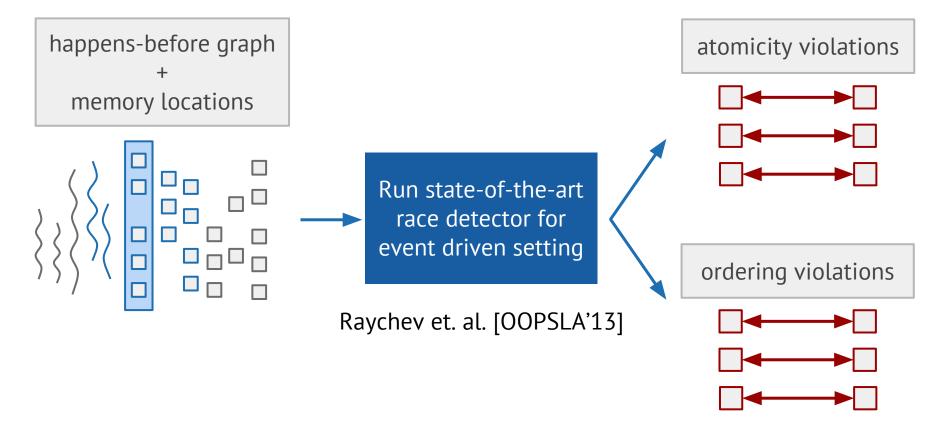
EventOp CallbackReg#1 CallbackReg#2 CallbackUnreq CallbackInv MsqBegin#1 MsgBegin#2 LooperAtomic MsgEnqueue ThreadFork ThreadJoin ThreadInit ThreadExit NotifyWait MsqRemove MsgBlocking Native IpcHandle ThreadOp IpcAsync



first scalable algorithm for building rich happens-before graph for whole Android system



How to make scalable race detection in event-based setting?





INSTRUMENTED

SYSTEM



APPLICATION EXPLORATION



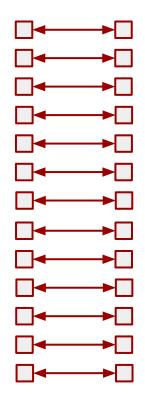
HAPPENS-BEFORE

GRAPH BUILDING

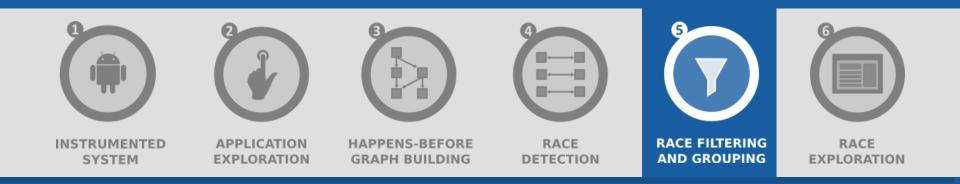
RACE DETECTION

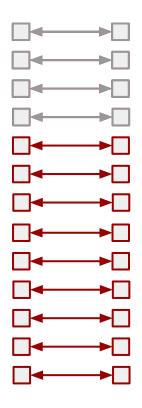
5 **RACE FILTERING** AND GROUPING

RACE EXPLORATION



1328



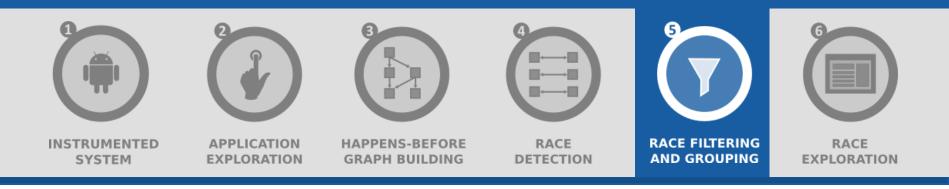


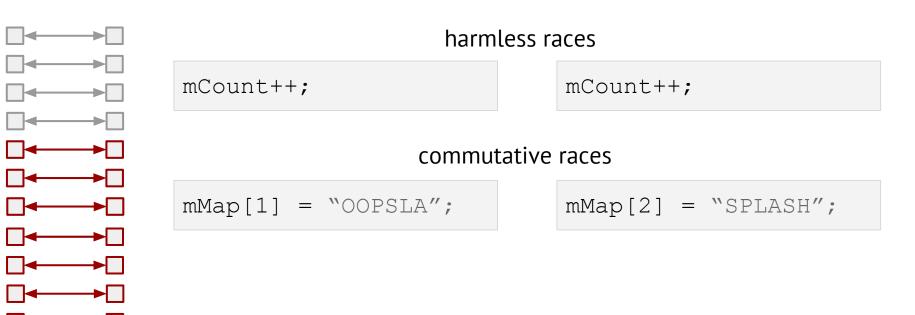
1328

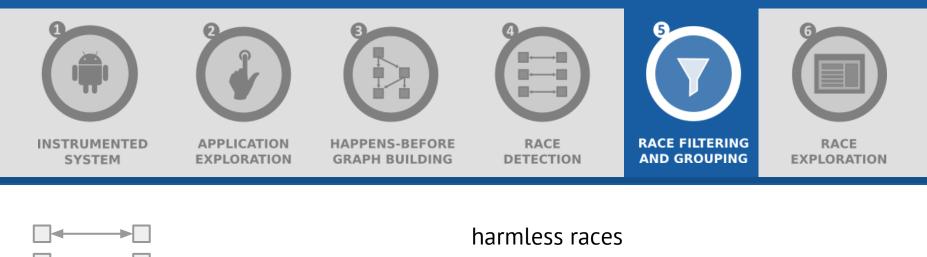
harmless races

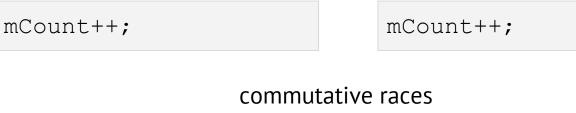
mCount++;

mCount++;









mMap[1] = "OOPSLA";

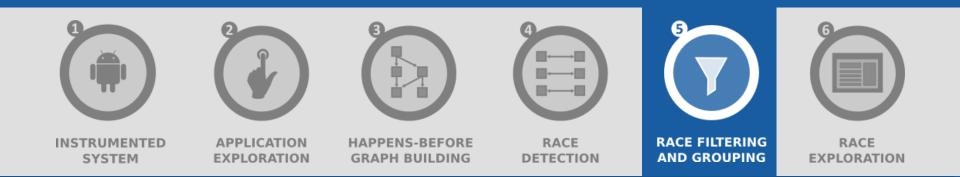
mMap[2] = "SPLASH";

synchronization races

if (mActive) return;

mActive = true;

1328



100x reduction of reported concurrency conflicts $(1328 \rightarrow 13)$

Manual evaluation

Analysis Scalability

354 Play Store Applications



#events	~28 000
#happens-before operations	~590 000
#memory locations	~5 140 000
analysis runtime	70s - 130s

10 minutes application usage

Manual evaluation

Usability

8 Play Store Applications



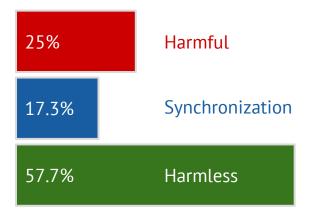








Main Application Thread (10625 races \rightarrow 104 reports)



Other Threads (2804 races \rightarrow 135 reports)

Related Work

CAFA [Hsiao et.al, PLDI'14] & DroidRacer [Maiya et.al PLDI'14]

Analysis Scalability

Metric	Our Work	CAFA & DroidRacer
Exploration time	10 min	10 - 30 s
Analysis time	70 - 130 s	30 min - 1 day

More precise happens-before model:

- → Complete handling of message types
- → Message removal
- → Effect of barriers
- → More precise IPC communication

Error Coverage & Usability

- → [CAFA] Null pointer dereference + usability
 - missed bugs
- → [DroidRacer] Application code without filtering + better bug coverage
 - poor usability (too many races reported)

→ [our work] User + Framework code with filtering

- + complete bug coverage
- + usability (100x report reduction)



SYSTEM



APPLICATION EXPLORATION



HAPPENS-BEFORE GRAPH BUILDING



RACE DETECTION



RACE FILTERING

AND GROUPING



RACE EXPLORATION

